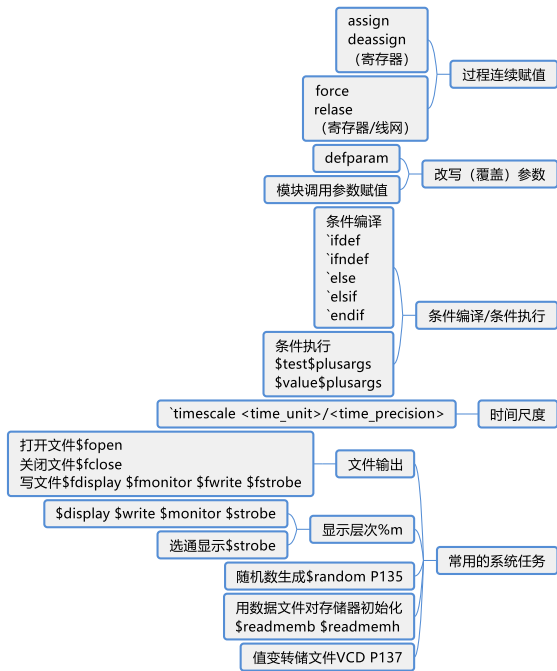


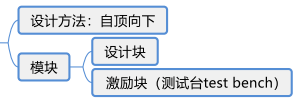
VerilogHDL

《Verilog HDL: A Guide to Digital Design and Synthesis》
第二版 [美] Samir Palnitkar 著

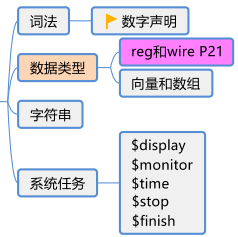
实用建模技术



层次建模



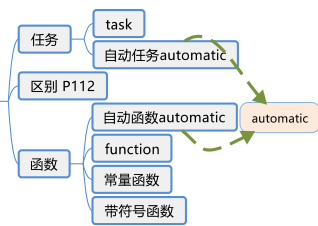
基本概念



模块和端口

端口连接规则 P35图

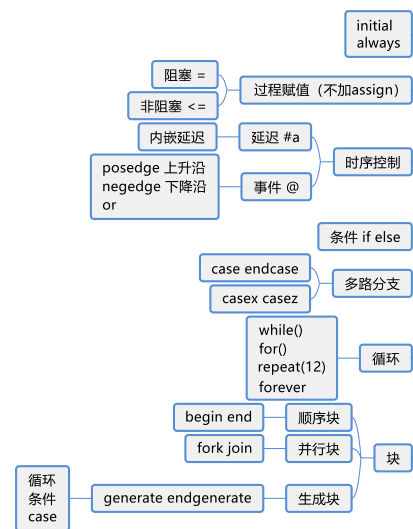
任务及函数



拓展内容



行为级建模



数据流建模



门级建模

